

**500V 5A 1.35Ω N-ch Power MOSFET****Description**

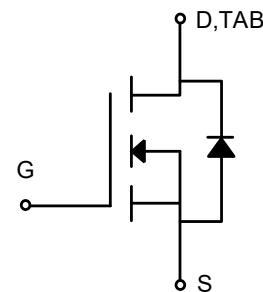
WMOS™ D1 is Wayon's 1<sup>st</sup> generation VDMOS family that is dramatic reduction in on-resistance and ultra-low gate charge for applications requiring high power density and high efficiency. And it is very robust and RoHS compliant.

**TO-252****Features**

- Typ. $R_{DS(on)}=1.35\Omega$ @ $V_{GS}=10V$
- 100% avalanche tested
- Pb-free, Halogen free

**Applications**

- SMPS
- Charger
- DC-DC

**Absolute Maximum Ratings (T<sub>c</sub>=25°C)**

Parameter	Symbol	WMO5N50D1B	Unit
Drain-source voltage	$V_{DSS}$	500	V
Gate-source voltage	$V_{GS}$	$\pm 30$	V
Continuous drain current	$I_D$	5	A
Pulsed drain current <sup>1</sup>	$I_{DM}$	20	A
Avalanche energy, single pulse <sup>2</sup>	$E_{AS}$	125	mJ
Power dissipation	$P_D$	45	W
Derate above 25°C		0.36	W/°C
Operating junction temperature	$T_j$	-55~150	°C
Storage temperature	$T_{stg}$	-55~150	°C
Continuous diode forward current	$I_S$	5	A
Diode pulse current	$I_{Spulse}$	20	A

**Thermal Characteristic**

Thermal resistance,junction-to-case	$R_{\theta JC}$	2.8	°C/W
Thermal resistance,junction-to-ambient	$R_{\theta JA}$	62.5	°C/W

**Electrical Characteristics of MOSFET**

				T <sub>C</sub> =25°C	Min.	Typ.	Max.
Drain-source break down voltage	BV <sub>DSS</sub>	I <sub>D</sub> =250uA, V <sub>GS</sub> =0V		T <sub>C</sub> =25°C	500	-	-
Gate threshold voltage	V <sub>GS(th)</sub>	I <sub>D</sub> =250uA, V <sub>DS</sub> =V <sub>GS</sub>		T <sub>J</sub> =25°C	2.0	3.0	4.0
Drain-source leakage current	I <sub>DSS</sub>	V <sub>DS</sub> =500V, V <sub>GS</sub> =0V		T <sub>J</sub> =25°C	-	-	1
		V <sub>DS</sub> =400V, V <sub>GS</sub> =0V		T <sub>J</sub> =125°C	-	-	100
Gate-source leakage current,forward	I <sub>GSSF</sub>	V <sub>DS</sub> =0V, V <sub>GS</sub> =30V		T <sub>J</sub> =25°C	-	-	100
Gate-source leakage current,reverse	I <sub>GSSR</sub>	V <sub>DS</sub> =0V, V <sub>GS</sub> =-30V		T <sub>J</sub> =25°C	-	-	-100
Drain-source on-state resistance <sup>3</sup>	R <sub>DS(ON)</sub>	V <sub>GS</sub> =10V, I <sub>D</sub> =2.5A		T <sub>J</sub> =25°C	-	1.35	1.6

**Dynamic Characteristics of MOSFET** (T<sub>C</sub>=25°C)

					Min.	Typ.	Max.
Input capacitance	C <sub>iss</sub>	f=1MHz, V <sub>DS</sub> =25V, V <sub>GS</sub> =0V			-	597	-
Output capacitance	C <sub>oss</sub>				-	63	-
Reverse transfer capacitance	C <sub>rss</sub>				-	6	-
Gate to source charge	Q <sub>gs</sub>	V <sub>DD</sub> =400V I <sub>D</sub> =5A V <sub>GS</sub> = 0 to 10V			-	7	-
Gate to drain charge	Q <sub>gd</sub>				-	6	-
Total gate charge	Q <sub>g</sub>				-	23	-

**Switching Characteristics of MOSFET** (T<sub>C</sub>=25°C)

					Min.	Typ.	Max.
Turn-on delay time	t <sub>d on</sub>	V <sub>DS</sub> =250V, I <sub>D</sub> =5A, R <sub>G</sub> =25Ω, V <sub>GS</sub> =0 to 10V			-	12	-
Rise time	t <sub>r</sub>				-	17	-
Turn-off delay time	t <sub>d off</sub>				-	40	-
Fall time	t <sub>f</sub>				-	19	-

**Characteristics of Body Diode** (T<sub>C</sub>=25°C)

					Min.	Typ.	Max.
Forward voltage	V <sub>SD</sub>	I <sub>SD</sub> =5A, V <sub>GS</sub> =0V			-	-	1.4
Reverse recovery time	t <sub>rr</sub>	I <sub>S</sub> =5A, V <sub>GS</sub> =0V di/dt=100A/us			-	168	-
Reverse recovery current	I <sub>rr</sub>				-	7.7	-
Recovery charge	Q <sub>rr</sub>				-	0.7	-

Notes:

1. Repetitive rating, pulse width limited by junction temperature T<sub>J(MAX)</sub> =150°C.
2. The EAS data shows Max. rating . The test condition is V<sub>DD</sub> =50V, V<sub>GS</sub> =10V, L=10mH, I<sub>AS</sub> =5A, T<sub>C</sub>=25°C.
3. The data tested by pulsed , pulse width ≤ 300us , duty cycle ≤ 2%.

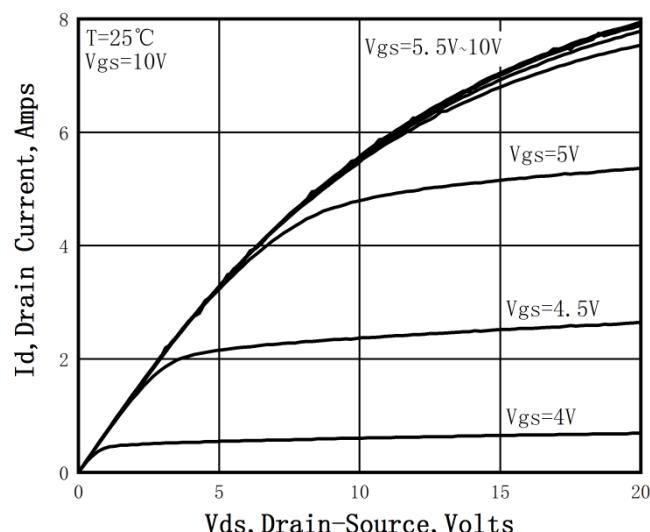


Figure 1. On-Region Characteristics

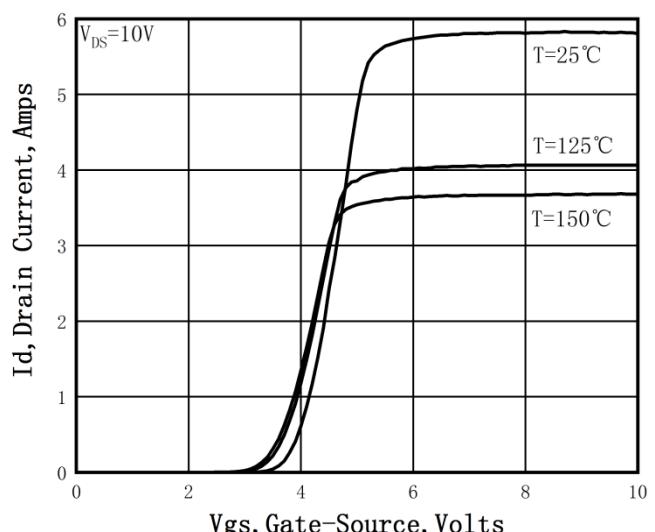


Figure 2. Transfer Characteristics

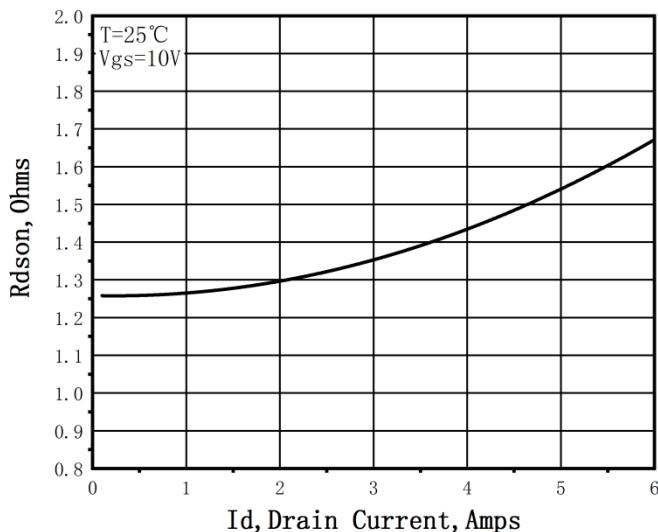


Figure 3. Static Drain-Source On Resistance

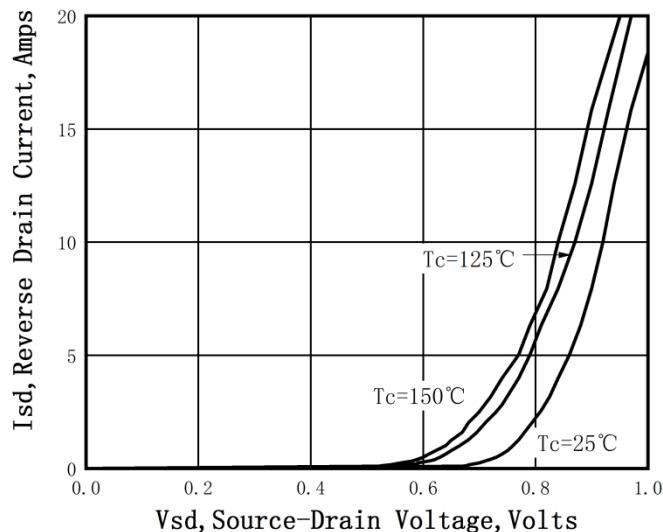


Figure 4. Typical Body Diode Transfer Characteristics

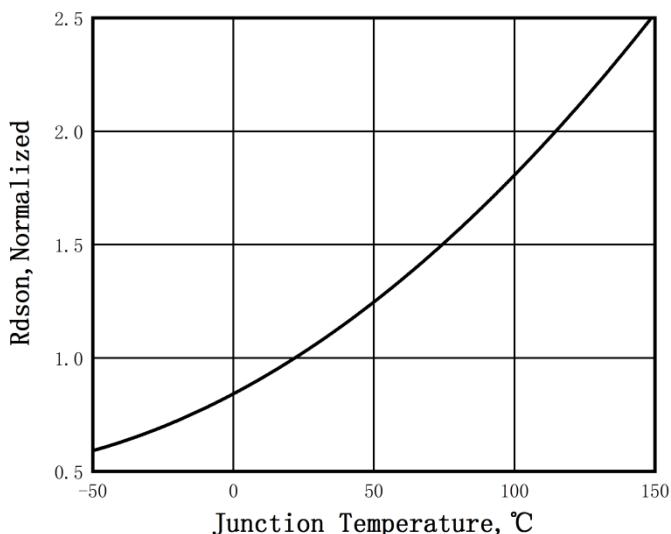


Figure 5. Normalized  $R_{ds(on)}$  vs. Temperature

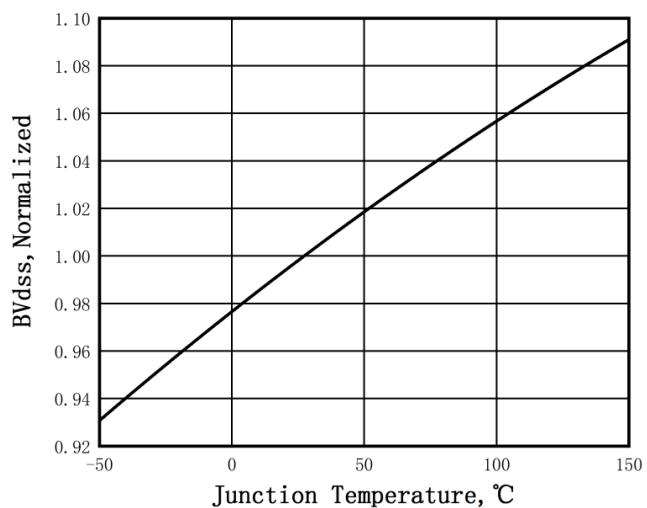
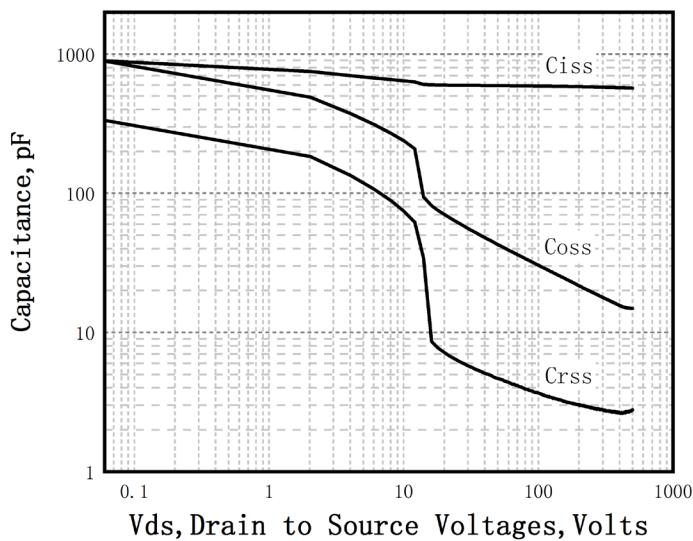
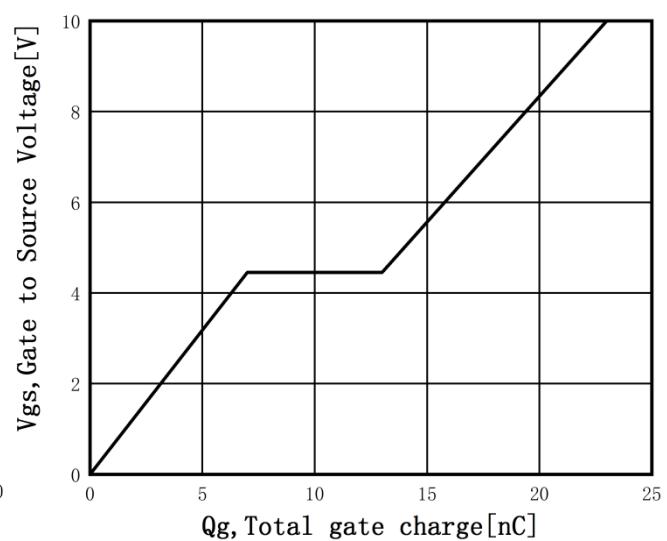


Figure 6. Normalized  $BV_{dss}$  vs. Temperature



V<sub>DS</sub>, Drain to Source Voltages, Volts

Figure 7. Capacitance Characteristics



Q<sub>G</sub>, Total gate charge [nC]

Figure 8. Gate Charge Characteristics

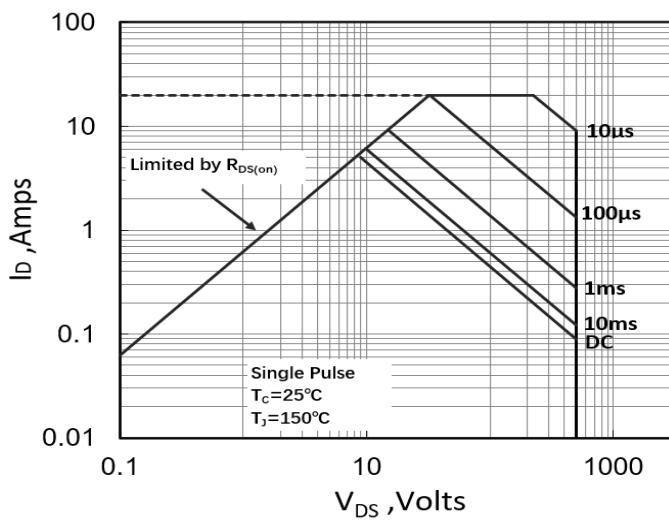


Figure 9. Maximum Safe Operating Area

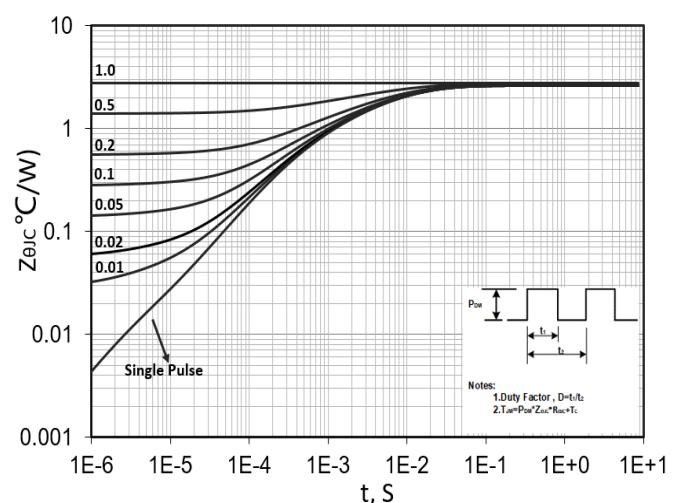
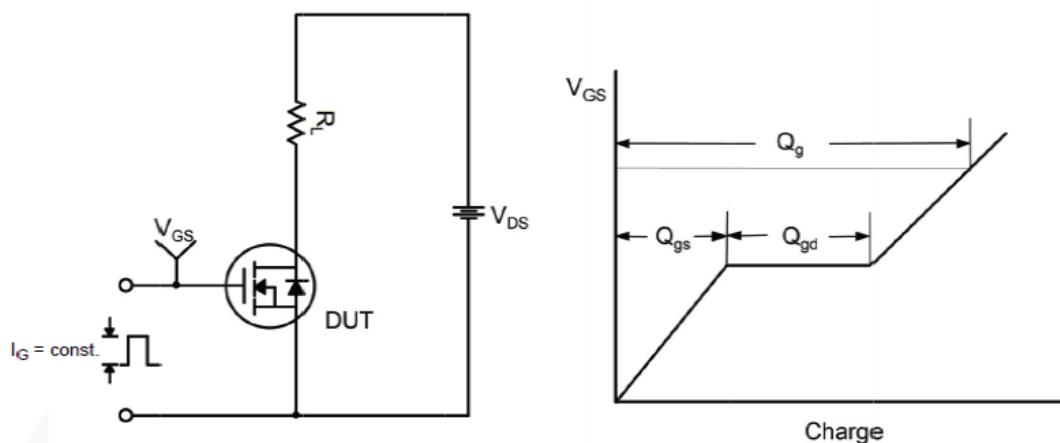
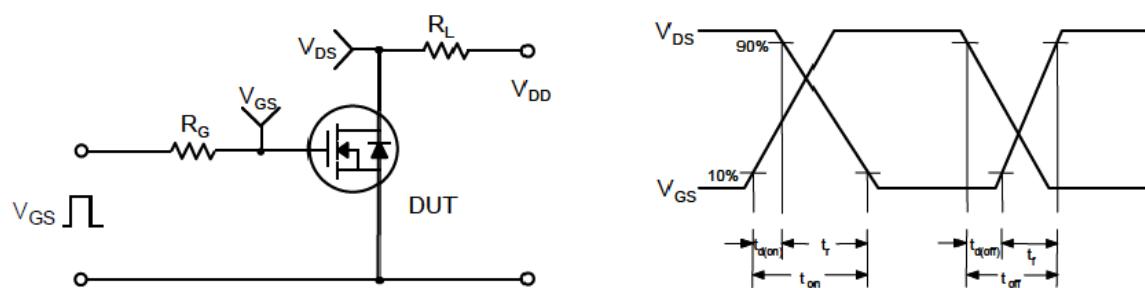


Figure 10. Transient Thermal Response Curve

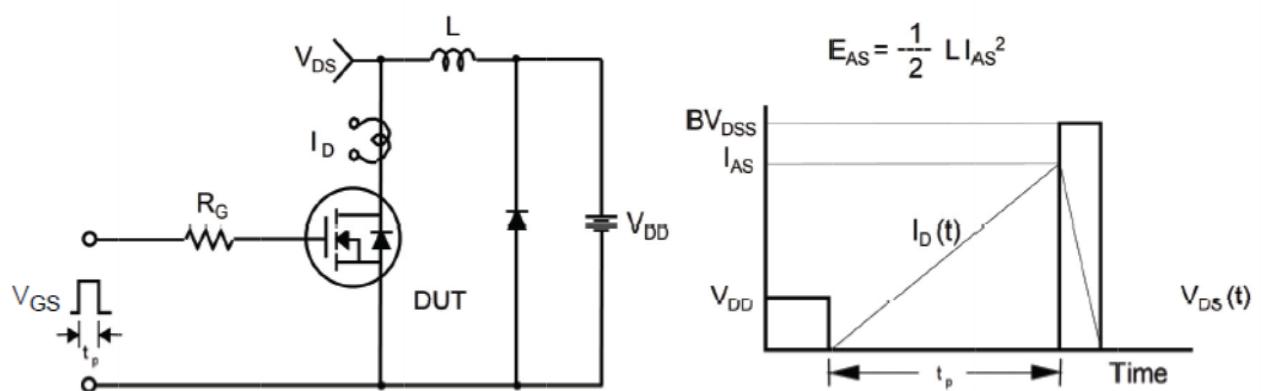
Gate Charge Test Circuit &amp; Waveform

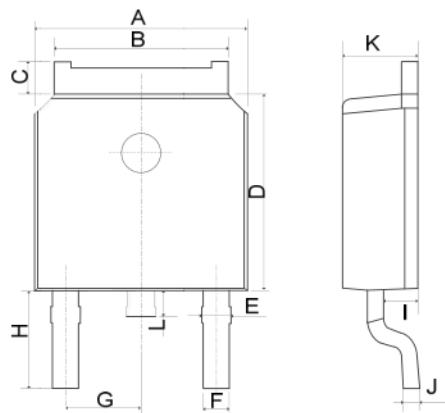


Switching Test Circuit &amp; Waveforms



Unclamped Inductive Switching Test Circuit &amp; Waveforms



**Mechanical Dimensions for TO-252****COMMON DIMENSIONS**

SYMBOL	MM	
	MIN	MAX
A	6.40	6.80
B	5.13	5.50
C	0.88	1.28
D	5.90	6.22
E	0.68	1.10
F	0.68	0.91
G	2.29REF	
H	2.90REF	
I	0.85	1.17
J	0.51REF	
K	2.10	2.50
L	0.40	1.00

**Ordering Information**

Part	Package	Marking	Packing method
WMO5N50D1B	TO-252	WMO5N50D1B	Tape and reel

